



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. 19516/159

patent application of
Ryan FEEMSTER et al.

Serial No. 08/113,299

Filed: August 30, 1993

For: INTER-PROCESSOR COMMUNICATION VIA POST OFFICE RAM

Group Art Unit: 2315
Examiner: R. Harrell

#12/CASE
W. Dawson
6/12/96

Assistant Commissioner for Patents
Washington, D.C. 20231

Box AF

Sir:

In response to the Office Action mailed February 5, 1996,
please amend the above-identified application as follows:

IN THE TITLE:

Please cancel the current title and insert therefor:

--DEVICE AND METHOD FOR INTERPROCESSOR COMMUNICATION USING
MAILBOXES OWNED BY PROCESSOR DEVICES--

IN THE CLAIMS:

Please amend the claims as follows:

1. (Twice Amended) An apparatus for communication between a plurality of processor devices, comprising:
a post office memory including a plurality of mailbox memories;

each of the mailbox memories being write accessible at any time only by a corresponding owner processor device, and read-accessible by the corresponding owner processor device and other processor devices of the plurality of processor devices at times determined independently of a write access;

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